

31000 U.S. PTO
09/852839



324	713	Subclass
Class	ISSUE CLASSIFICATION	

JPM

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
HU SCANNED 1/15-3 O.A. Am	

PATENT NUMBER

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/852839	F	438 324	1581	2812 2816	14 Vincent Nguyen

APPLICANTS

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324 713 2829
58

TITLE

Semiconductor integrated circuit, test method for the same, and recording device and communication equipment having the same

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
324	713	324	719				
INTERNATIONAL CLASSIFICATION		360	46				
G01R	27/08						
H01L	21/31						

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 18	Figs. Drwg. 20	Print Fig. 1	Total Claims 18	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	VINCENT Q. NGUYEN 8/19/03 (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED 09/09/03	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	N. Lo Supervisory Patent Examiner Technology Center 2800 9/63 (Primary Examiner) (Date)			ISSUE FEE Amount Due \$1600 Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	J. Miao 9/19/03 (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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